

**TIL111X, TIL114X, TIL116X, TIL117X
TIL111, TIL114, TIL116, TIL117**

**OPTICALLY COUPLED
ISOLATOR
PHOTOTRANSISTOR OUTPUT**



APPROVALS

- UL recognised, File No. E91231
- 'X' SPECIFICATION APPROVALS
- TIL111X is VDE 0884 approved in 3 available lead forms : -
 - STD
 - G form
 - SMD approved to CECC 00802

TIL114X, TIL116X, TIL117X : -

VDE 0884 pending

- TIL111X is certified to EN60950 by the following Test Bodies :-
Nemko - Certificate No. P96101299
Fimko - Registration No. 190469-01..22
Semko - Reference No. 9620076 01
Demko - Reference No. 305567
- TIL114X, TIL116X, TIL117X : -
EN60950 pending

DESCRIPTION

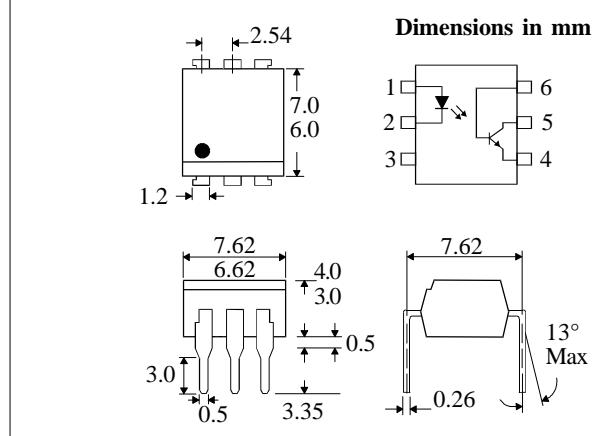
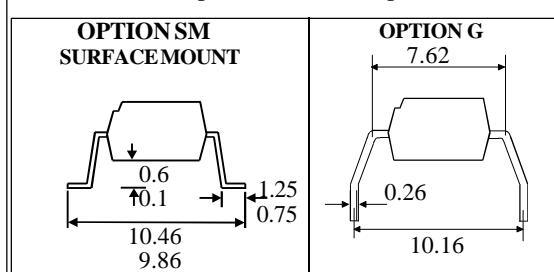
The TIL111, TIL114, TIL116, TIL117 series of optically coupled isolators consist of infrared light emitting diode and NPN silicon photo transistor in a standard 6 pin dual in line plastic package.

FEATURES

- Options :-
 - 10mm lead spread - add G after part no.
 - Surface mount - add SM after part no.
 - Tape&reel - add SMT&R after part no.
- High Isolation Voltage (5.3kV_{RMS}, 7.5kV_{PK})

APPLICATIONS

- DC motor controllers
- Industrial systems controllers
- Signal transmission between systems of different potentials and impedances



**ABSOLUTE MAXIMUM RATINGS
(25°C unless otherwise specified)**

Storage Temperature	-55°C to + 150°C
Operating Temperature	-55°C to + 100°C
Lead Soldering Temperature (1/16 inch (1.6mm) from case for 10 secs)	260°C

INPUT DIODE

Forward Current	60mA
Reverse Voltage	6V
Power Dissipation	105mW

OUTPUT TRANSISTOR

Collector-emitter Voltage BV _{CEO}	30V
Collector-base Voltage BV _{CBO}	70V
Emitter-collector Voltage BV _{ECD}	6V
Power Dissipation	160mW

POWER DISSIPATION

Total Power Dissipation	200mW
(derate linearly 2.67mW/°C above 25°C)	

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ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ Unless otherwise noted)

PARAMETER		MIN	TYP	MAX	UNITS	TEST CONDITION
Input	Forward Voltage (V_F) Reverse Voltage (V_R) Reverse Current (I_R)	6	1.2	1.4 10	V V μA	$I_F = 16\text{mA}$ $I_R = 10\mu\text{A}$ $V_R = 6\text{V}$
Output	Collector-emitter Breakdown (BV_{CEO}) Collector-base Breakdown (BV_{CBO}) Emitter-collector Breakdown (BV_{ECO}) Collector-emitter Dark Current (I_{CEO}) Collector-base Dark Current (I_{CBO}) Transistor Static Gain (h_{FE})	30 70 6 200		50 20	V V V nA nA	$I_c = 1\text{mA}$ (note 2) $I_c = 100\mu\text{A}$ $I_E = 100\mu\text{A}$ $V_{CE} = 10\text{V}$ $V_{CE} = 10\text{V}$ $10\text{mA } I_c, 5\text{V } V_{CE}$
Coupled	On-state Collector Current ($I_{C(on)}$) TIL111, TIL114 TIL116 TIL117 Collector-emitter Saturation Voltage $V_{CE(SAT)}$ TIL111, TIL114 TIL116 TIL117 Input to Output Isolation Voltage V_{ISO} 5300 7500 Input-output Isolation Resistance R_{ISO} 5×10^{10} Output Rise Time t_r Output Fall Time t_f	20 20 50 0.4 0.4 0.4 5300 V_{RMS} V_{PK} Ω 10 10			% % % V V V V_{RMS} V_{PK} Ω μs μs	$16\text{mA } I_F, 0.4\text{V } V_{CE}$ $10\text{mA } I_F, 10\text{V } V_{CE}$ $10\text{mA } I_F, 10\text{V } V_{CE}$ $16\text{mA } I_F, 2\text{mA } I_c$ $15\text{mA } I_F, 2.2\text{mA } I_c$ $10\text{mA } I_F, 0.5\text{mA } I_c$ See note 1 See note 1 $V_{IO} = 500\text{V}$ (note 1) $V_{CC} = 10\text{V}, I_c = 2\text{mA}$ $R_L = 100\Omega$ fig 1

Note 1 Measured with input leads shorted together and output leads shorted together.

Note 2 Special Selections are available on request. Please consult the factory.

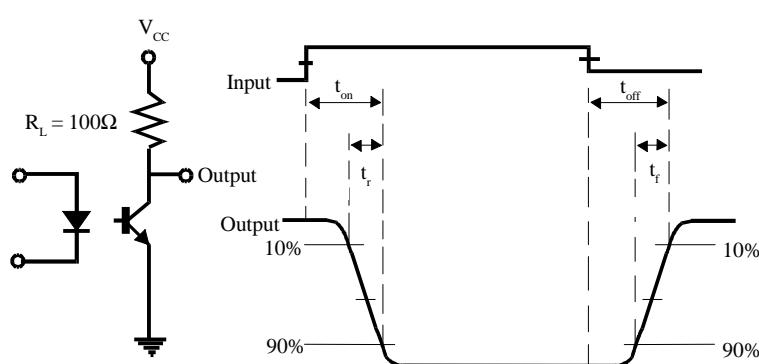
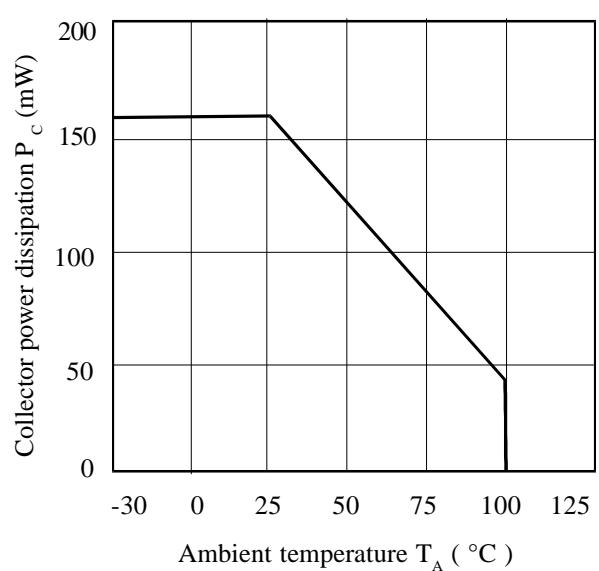
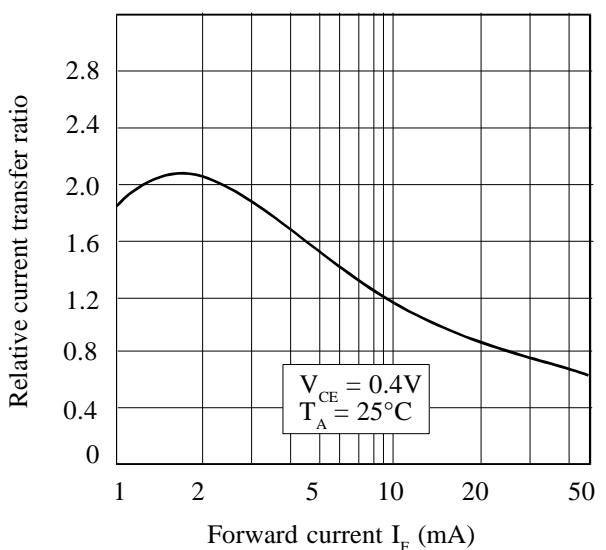


FIG 1

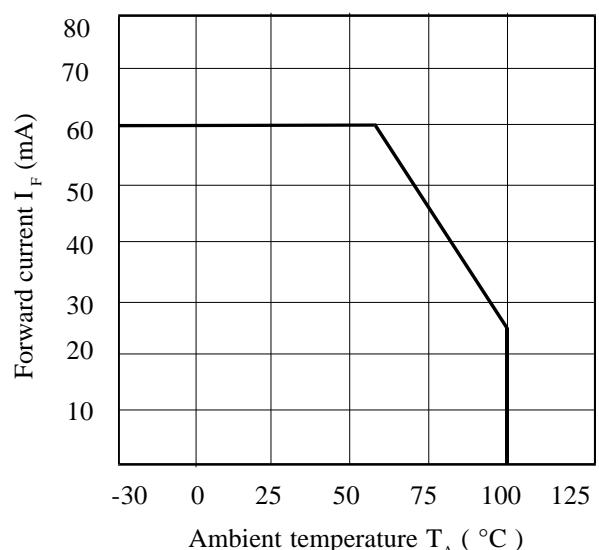
Collector Power Dissipation vs. Ambient Temperature



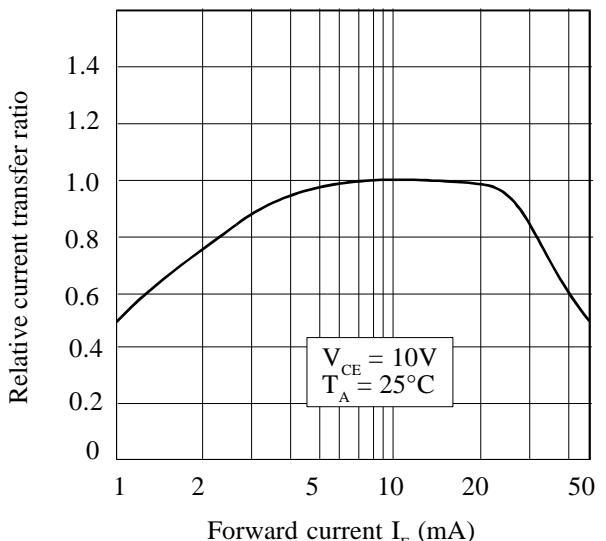
Relative Current Transfer Ratio vs. Forward Current (TIL111, TIL114)



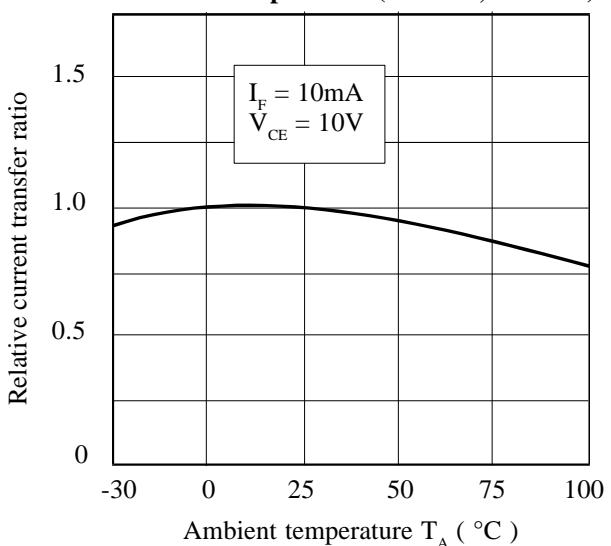
Forward Current vs. Ambient Temperature



Relative Current Transfer Ratio vs. Forward Current (TIL116, TIL117)



Relative Current Transfer Ratio vs. Ambient Temperature (TIL116, TIL117)



Relative Current Transfer Ratio vs. Ambient Temperature (TIL111, TIL114)

